

Figure 1A

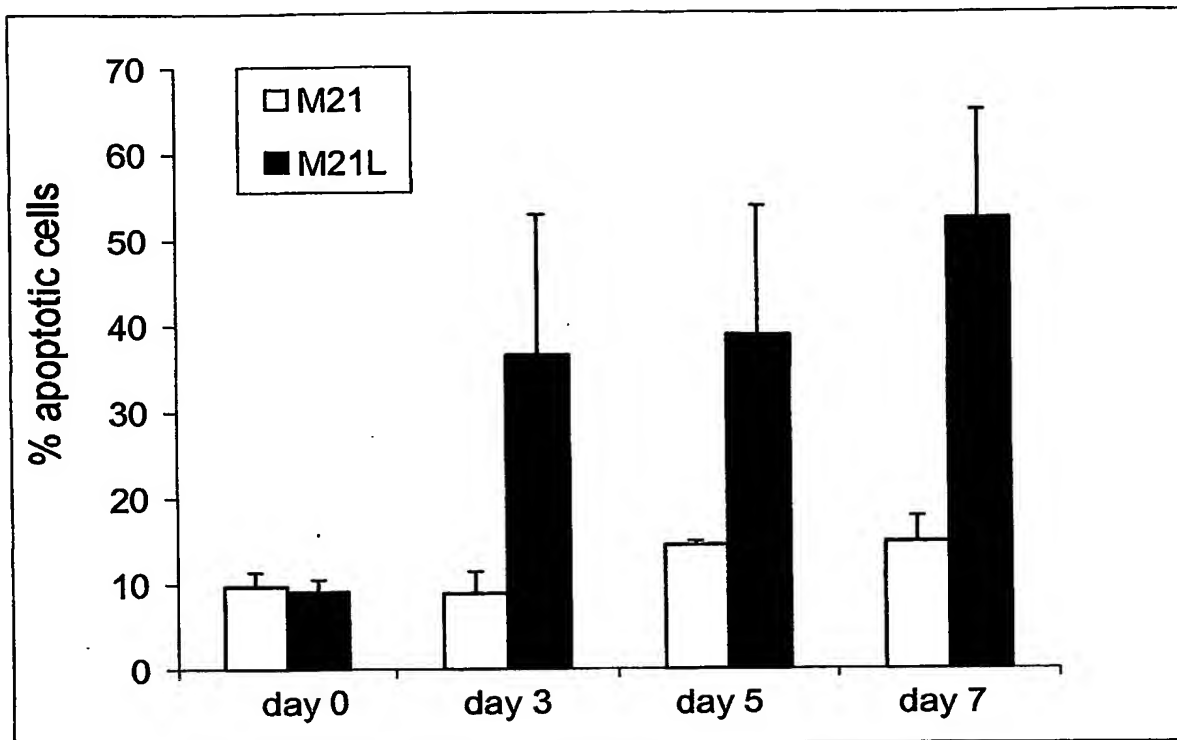


Figure 1B

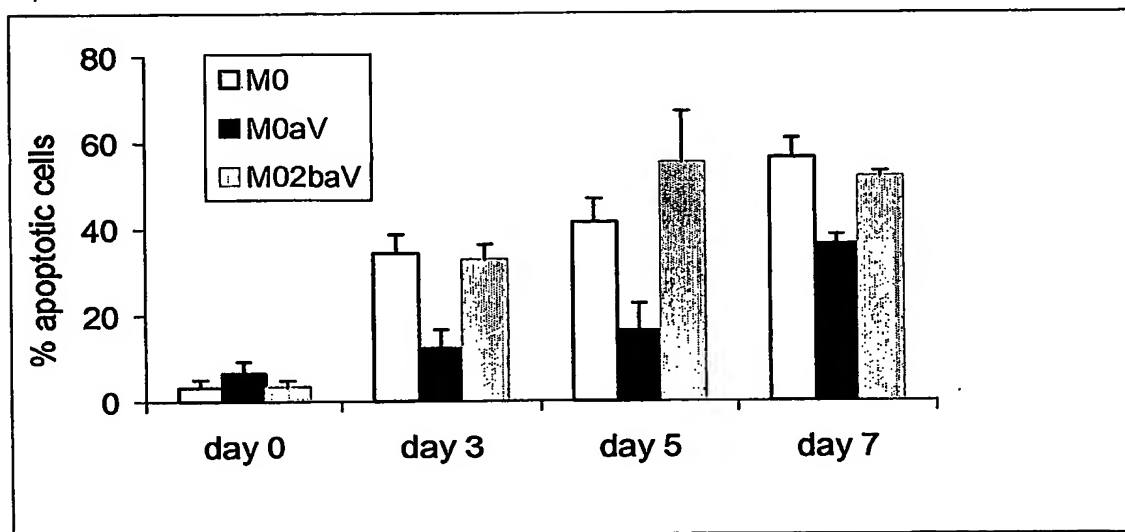


Figure 1C
Upper panel

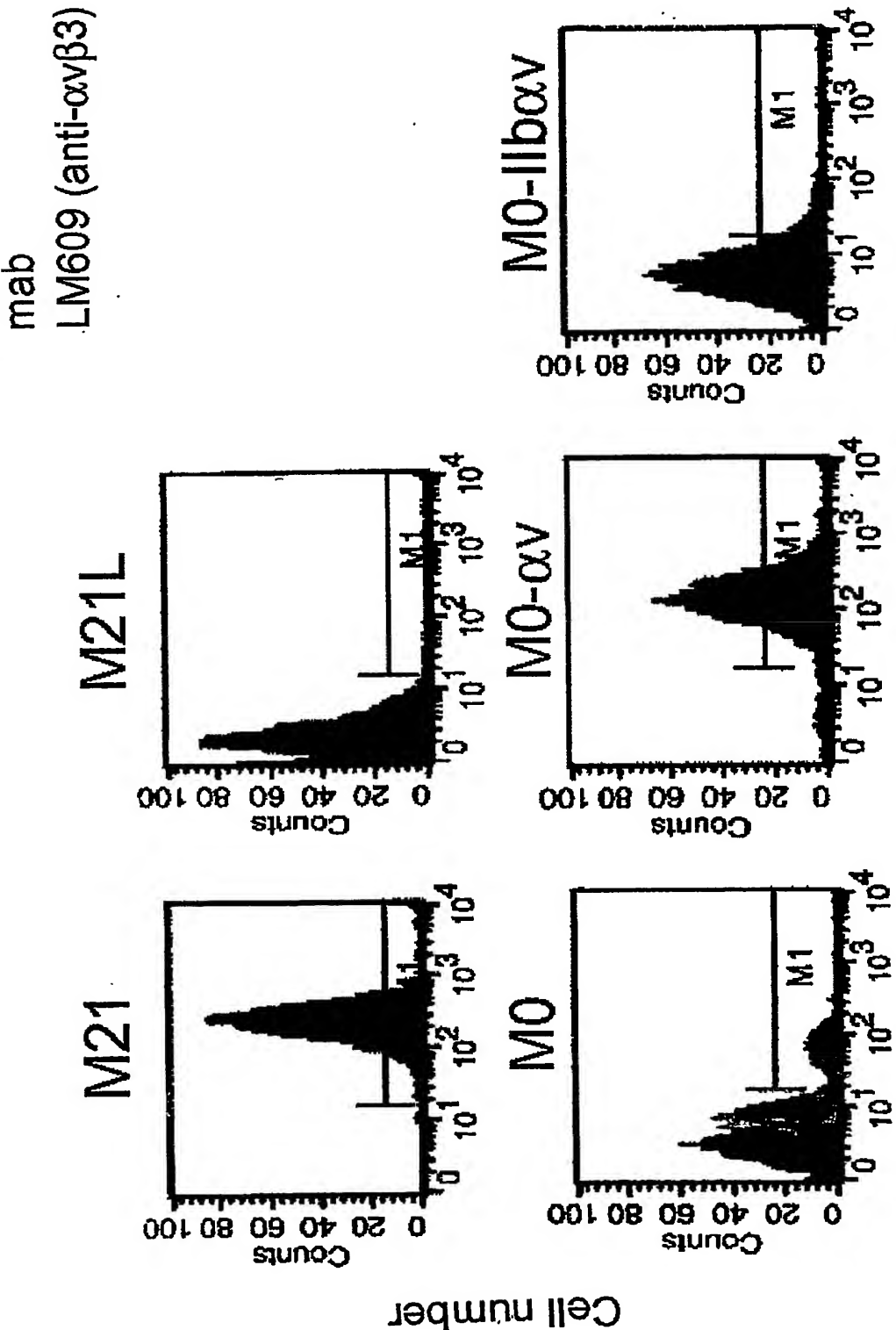


Figure 1C
Lower panel

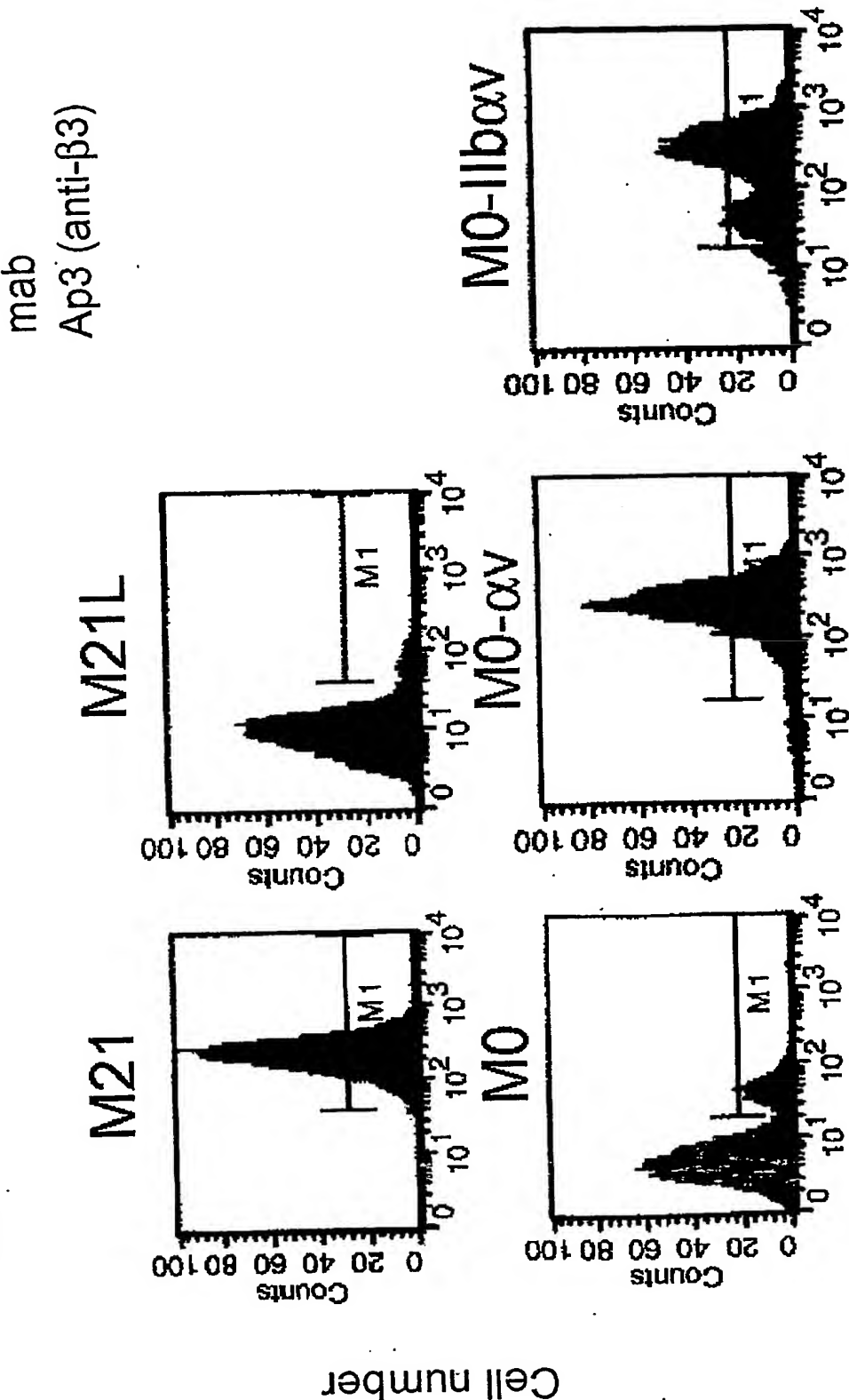


Figure 1D

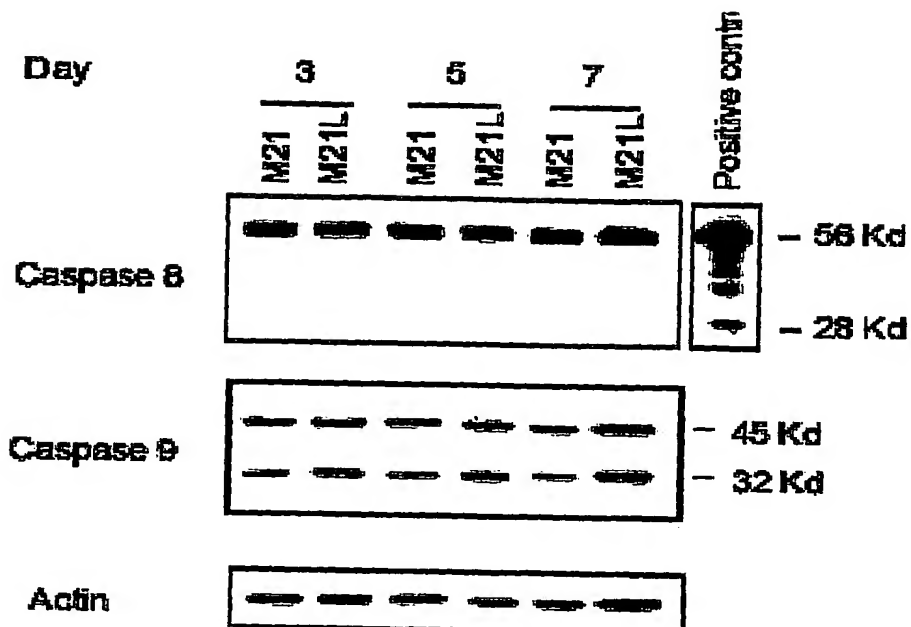


Figure 2A

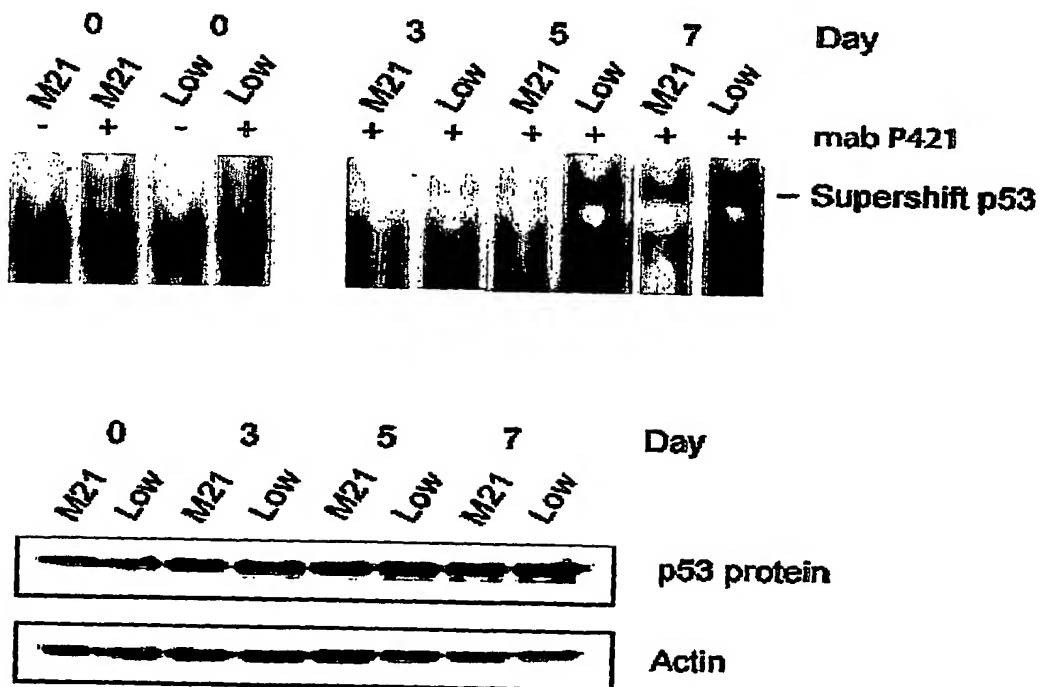


Figure 2B

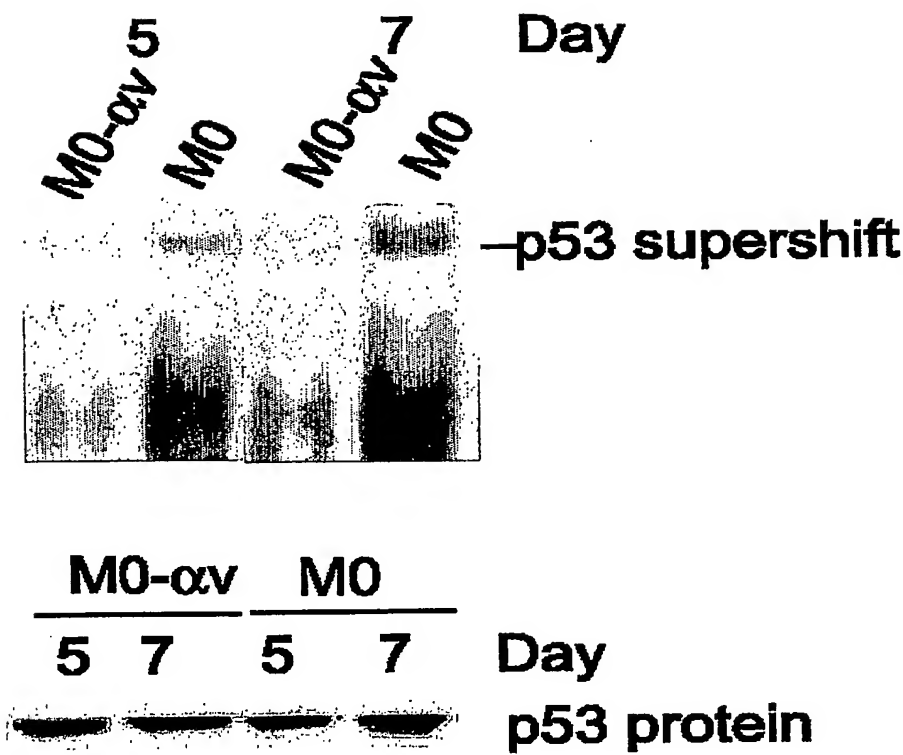


Figure 2C

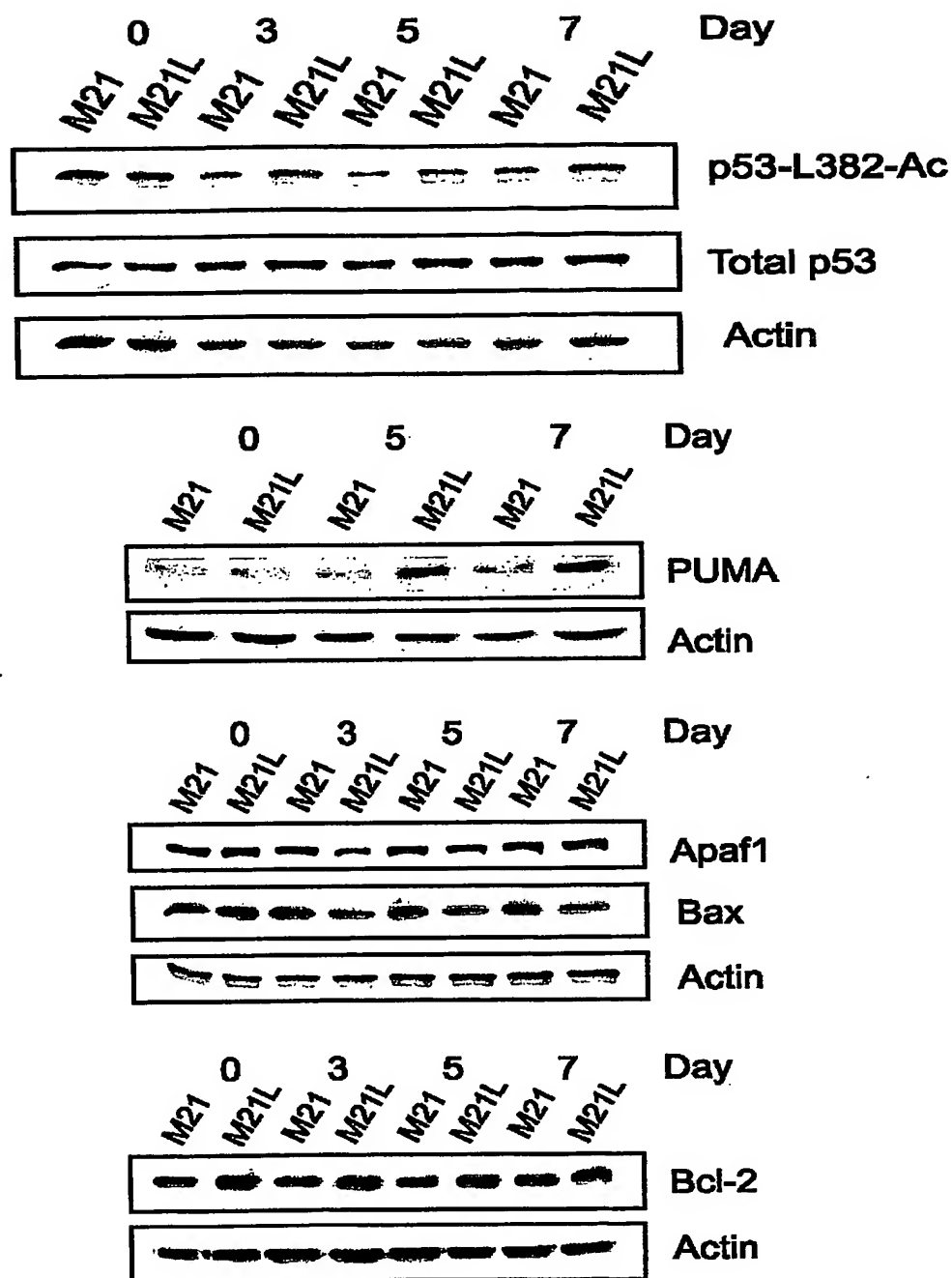


Figure 2D

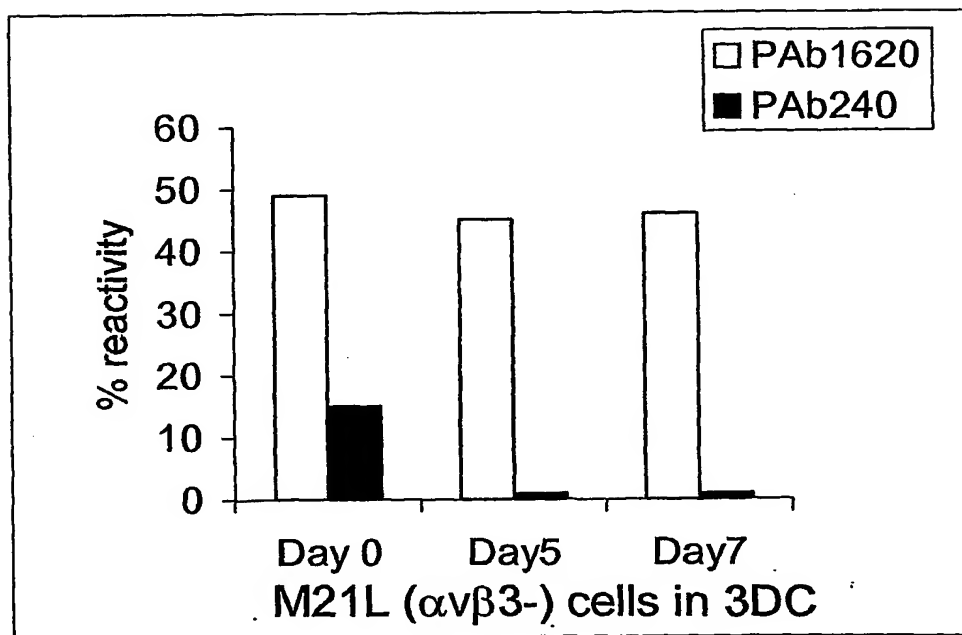
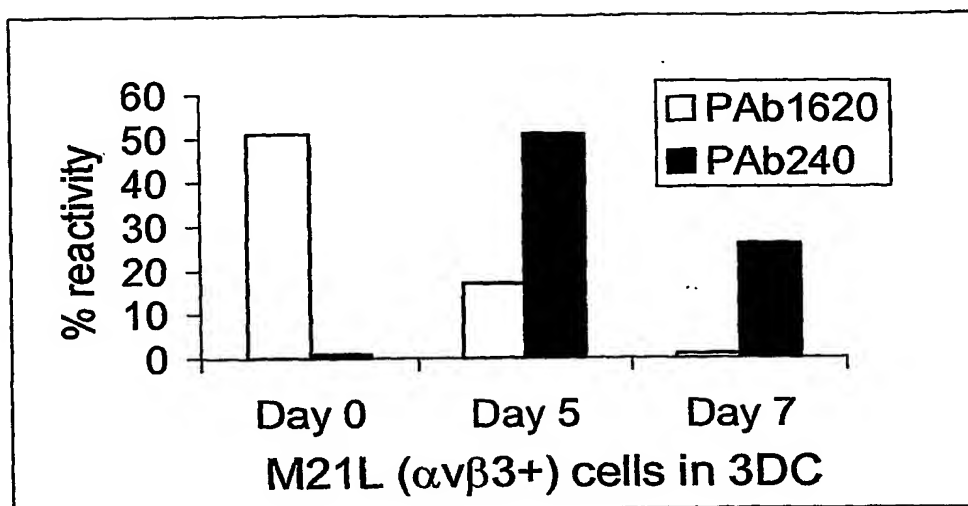


Figure 3A

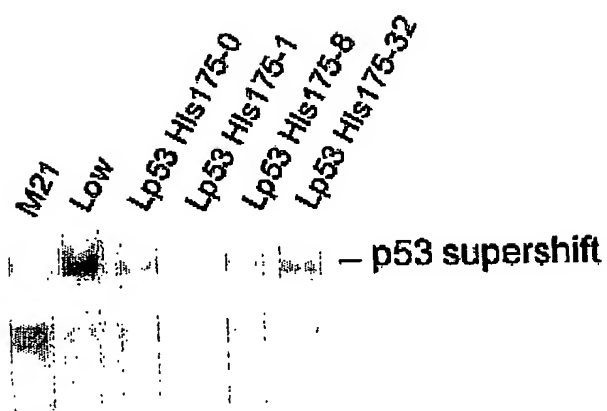
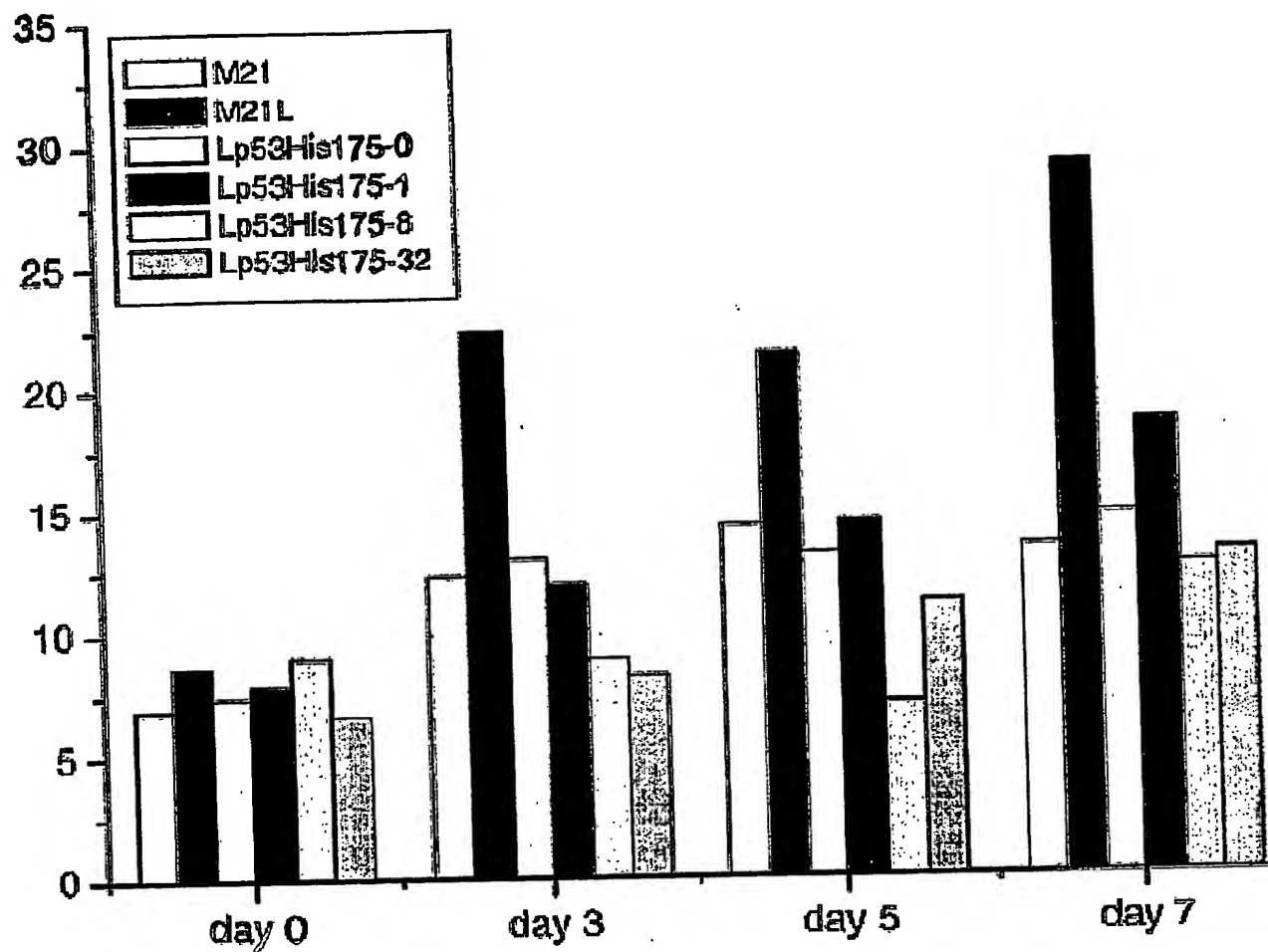


Figure 3B

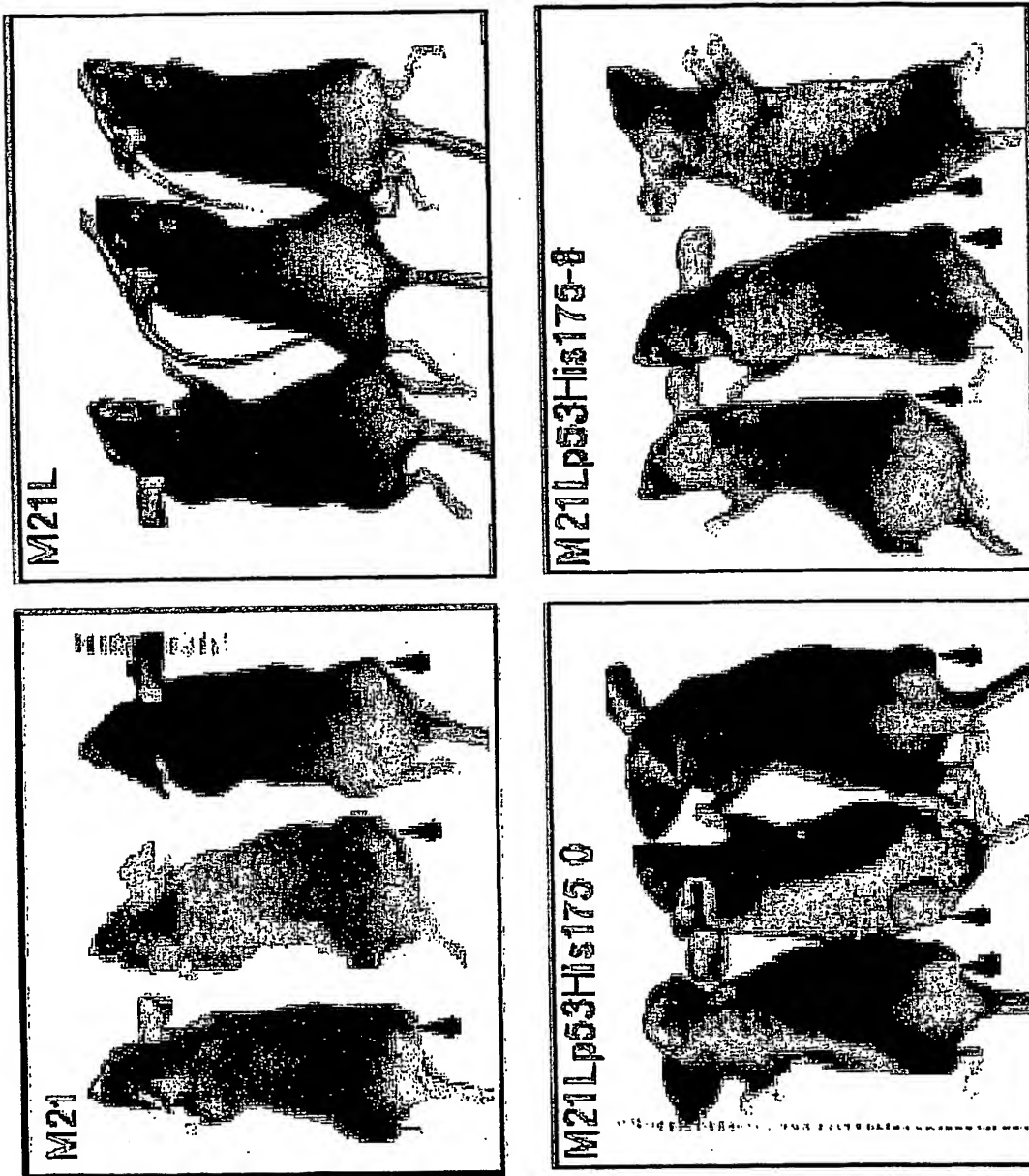


Figure 3C

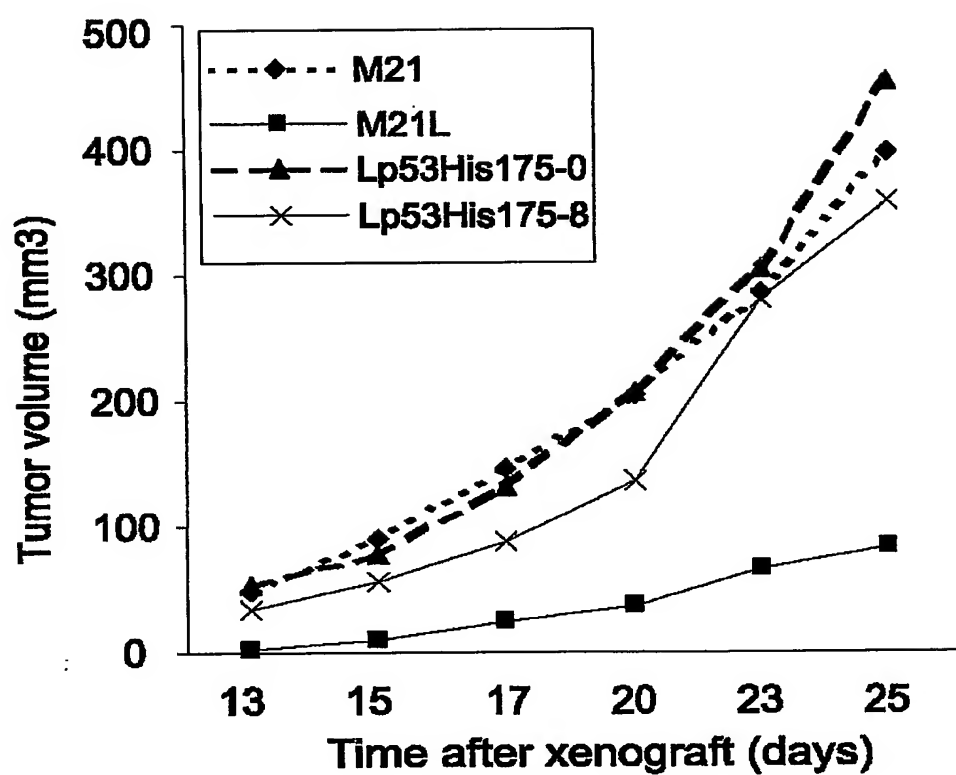


Figure 3D

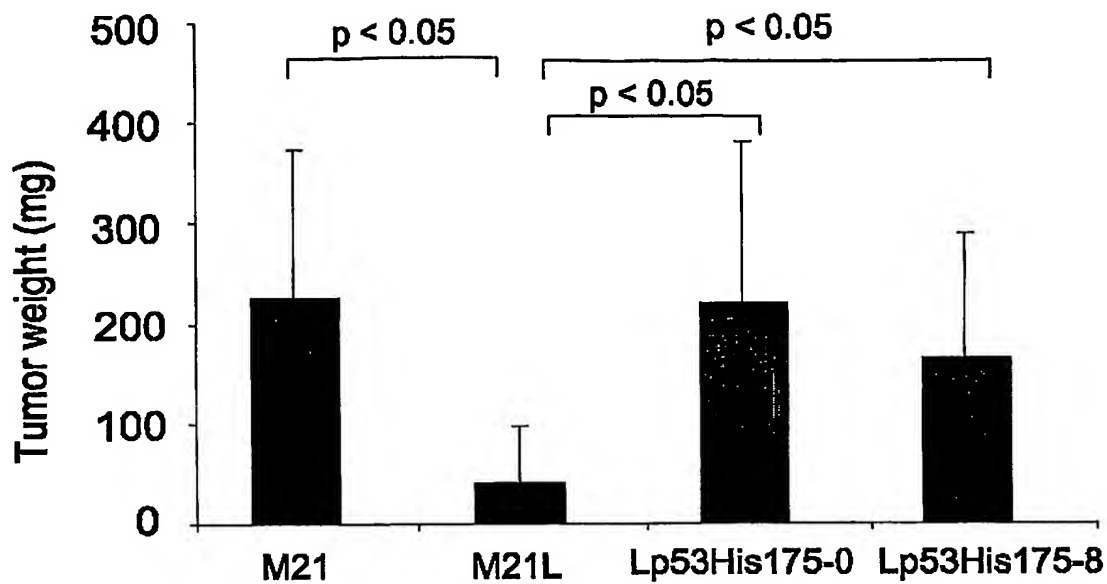


Figure 3E

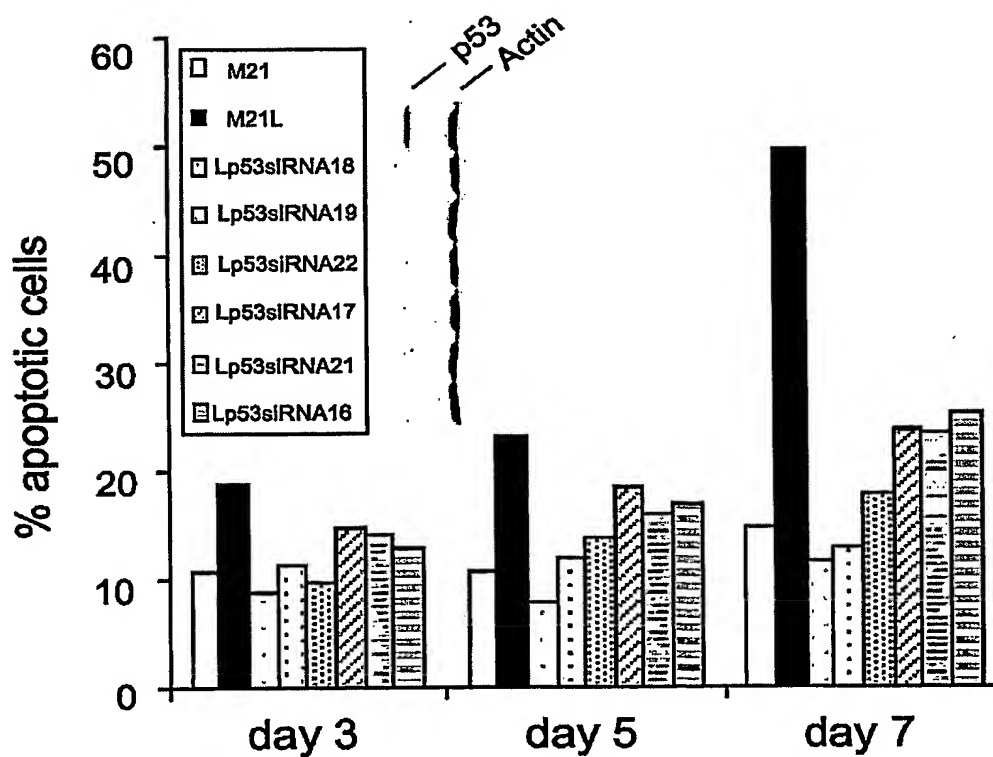


Figure 4A

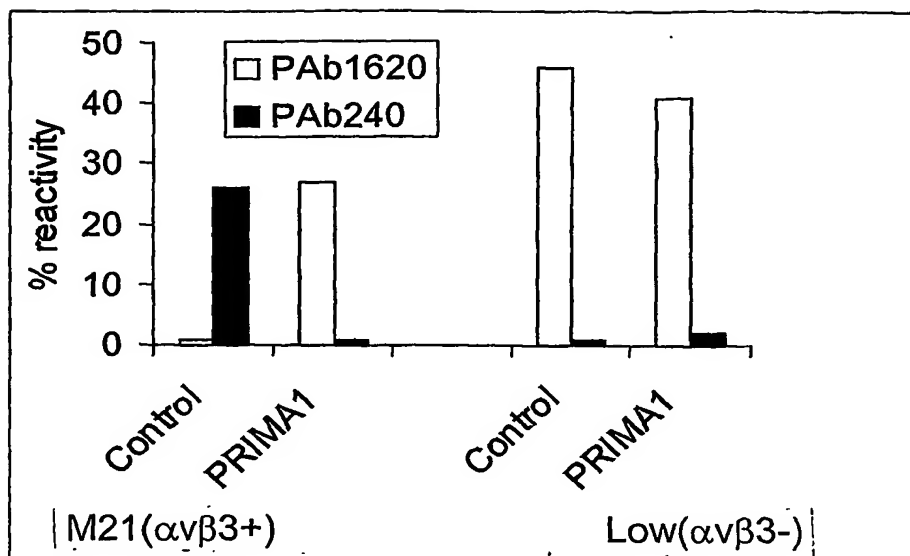


Figure 4B

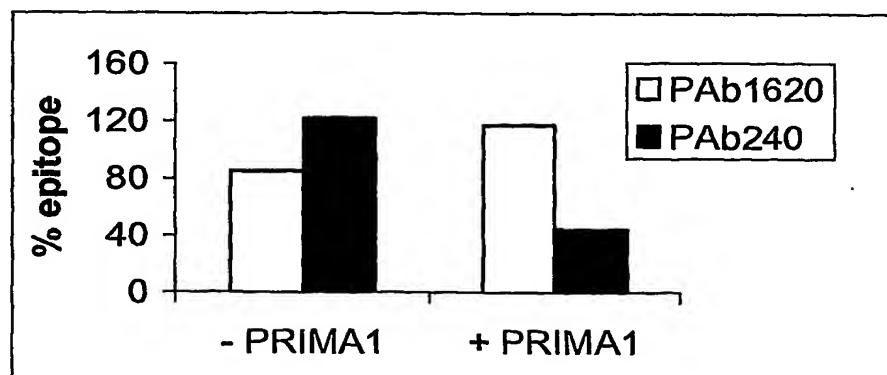
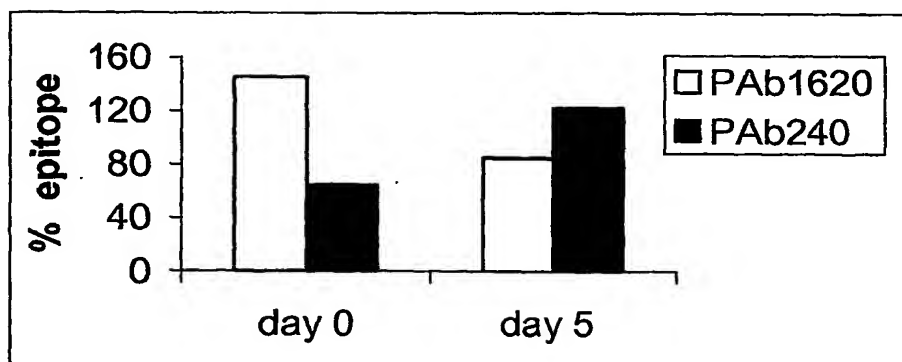
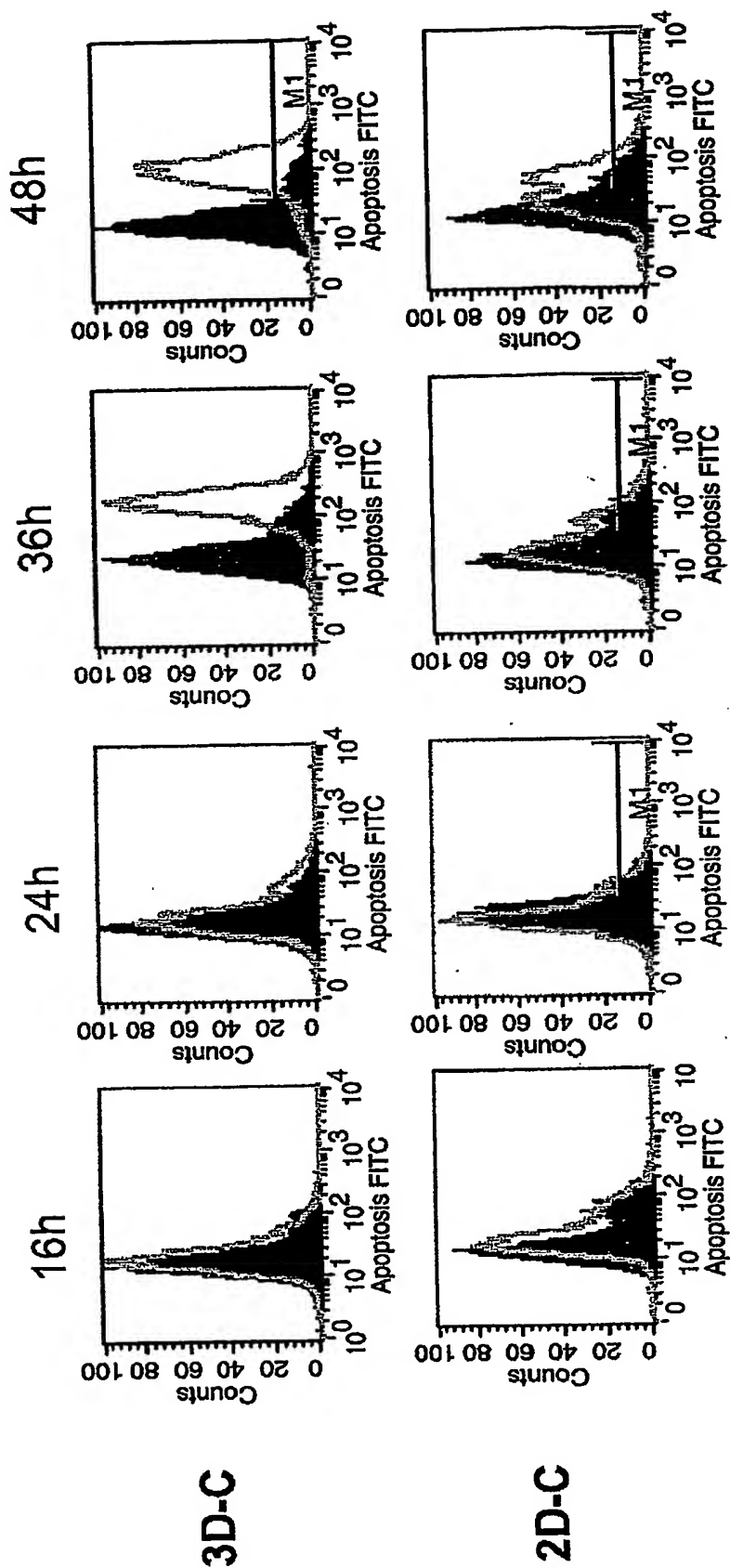


Figure 4C



+ PRIMA1
- PRIMA1

Figure 4D

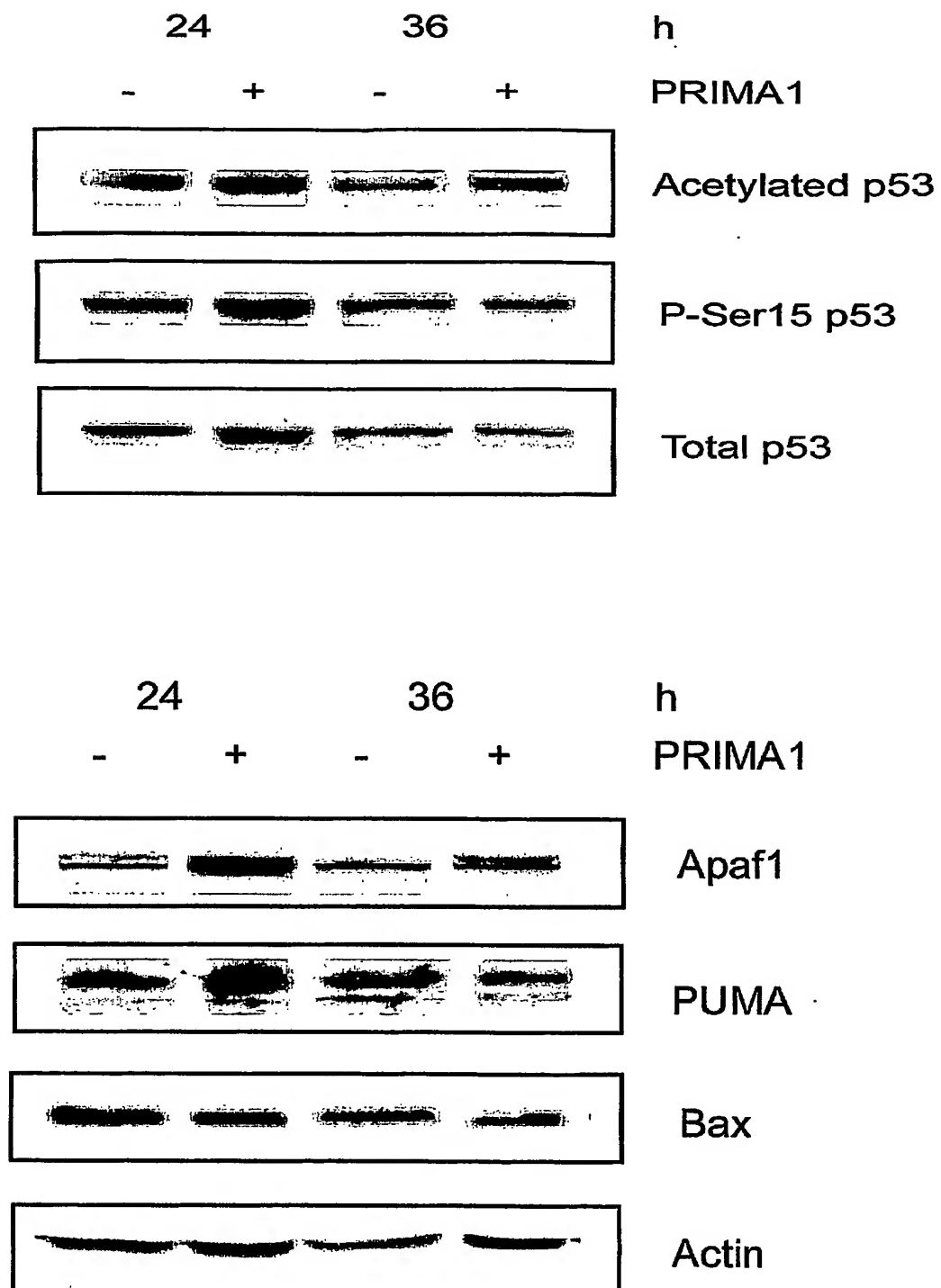


Figure 4E

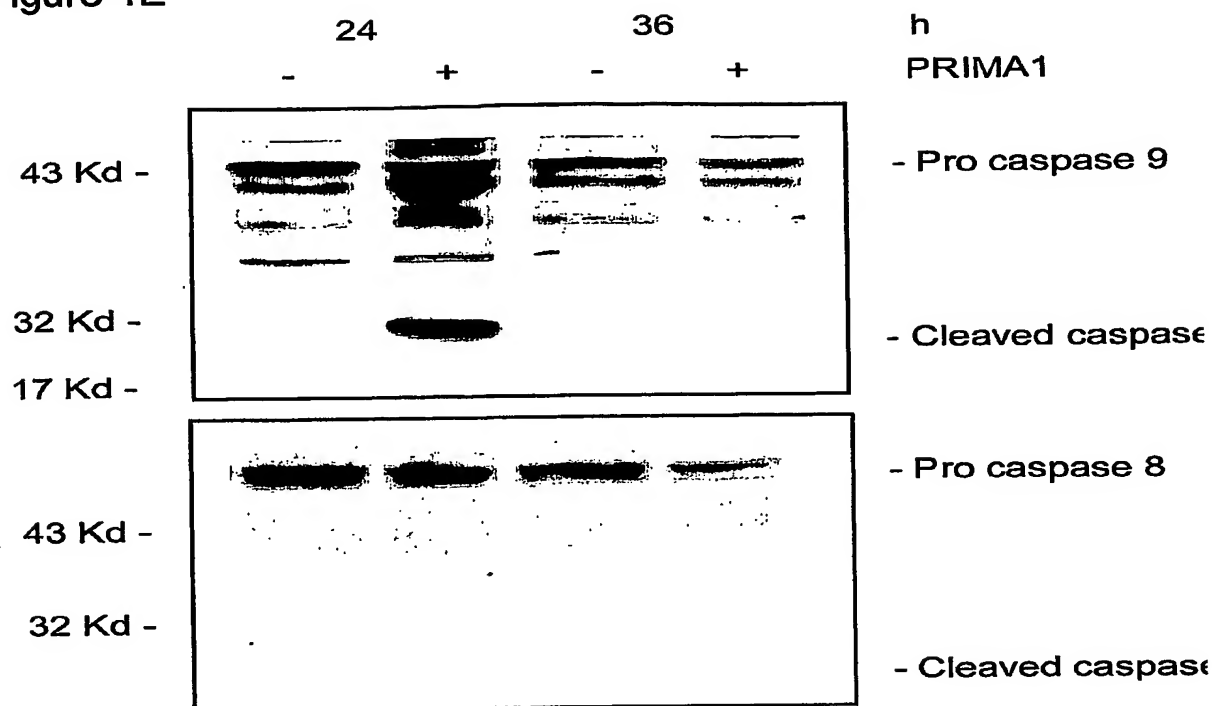


Figure 4F

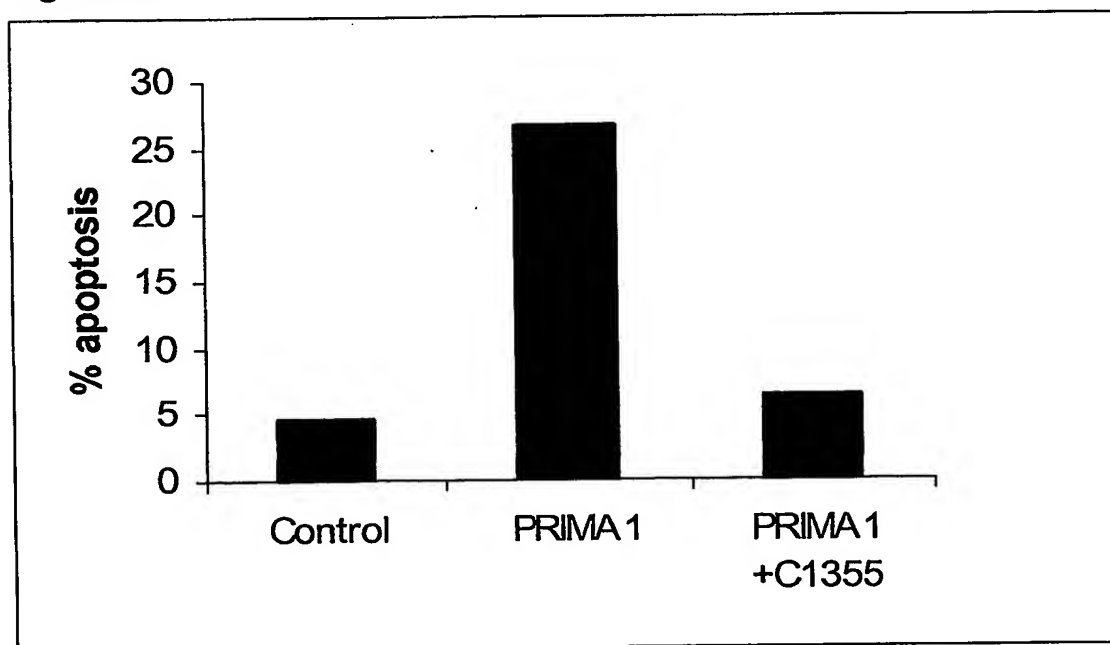


Figure 4 G

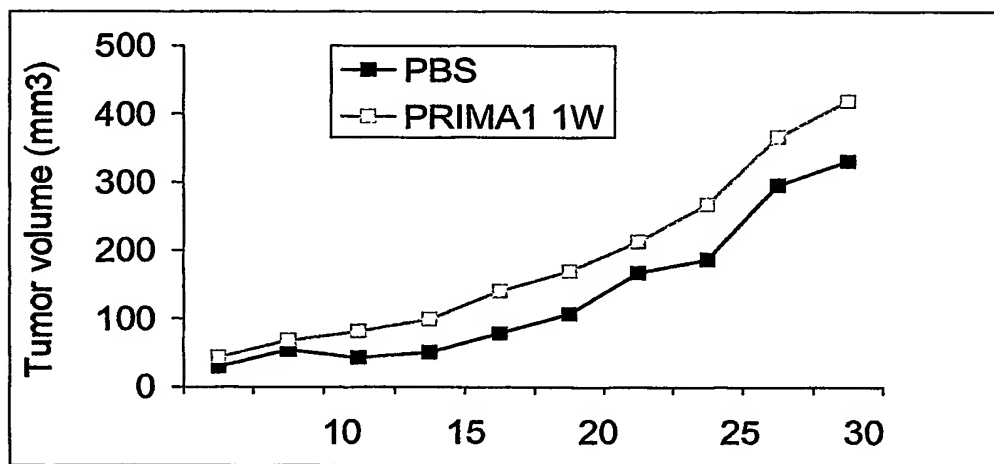
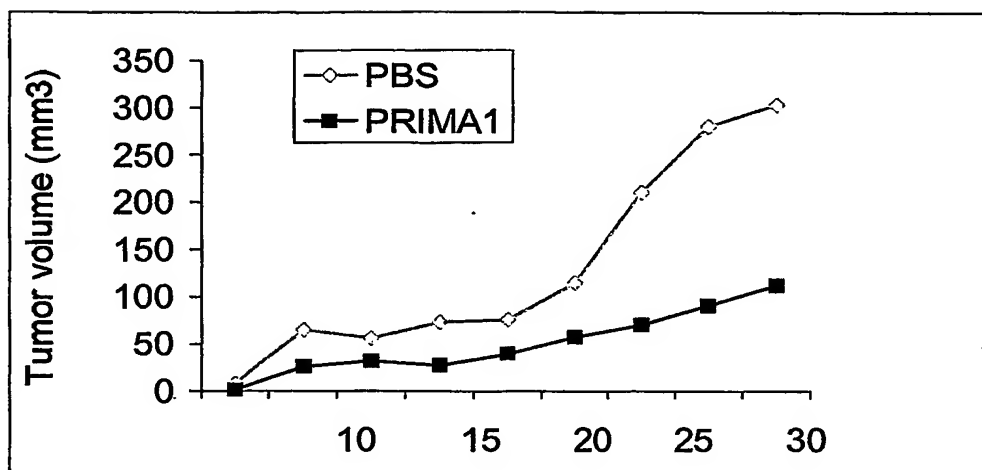


Figure 4H

